



ReCoSoC '07

June 18-20 Montpellier - France



Laboratoire
d'Informatique
de Robotique
et de Microélectronique
de Montpellier



TECHNISCHE
UNIVERSITÄT
DARMSTADT



Institute of
Microelectronic
Systems

Call for Papers

3rd International Workshop on Reconfigurable Communication Centric System-on-Chips

<http://www.lirmm.fr/RECOSOC07>

Submission deadline: May, 1st Notification of acceptance: June, 1st

Goals:

ReCoSoC aims to provide a prospective view of tomorrow's challenges in the multi-billion transistor era, taking into account the emerging techniques and architectures exploring the synergy between flexible on-chip communication and system reconfigurability.

Topics of interest include:

- Embedded Reconfigurability in all its forms including MPSoCs
- On-chip communication architectures
- Multiprocessor SoCs (MPSoCs)
- System & SoC design methods
- Asynchronous design techniques
- Low-power design methods
- Middleware and OS support for reconfiguration and communication
- New paradigms of computation including bio-inspired approaches

- **ReCoSoC** is a 3-day long event which endeavors to encourage scientific exchanges and collaborations. This year again ReCoSoC perpetuates its original principles: thanks to the high sponsoring obtained from our partners registration fees will remain under the 150 € threshold.

CONTACT

Dr. Gilles SASSATELLI, General Chair
LIRMM, CNRS / University of Montpellier II
Montpellier, FRANCE
sassatelli@lirmm.fr

Pr. Christophe BOBDA, Program Chair
Kaiserslautern University of Technology
Department of Computer Science
Self-Organizing Embedded Systems Group
Kaiserslautern, GERMANY
bobda@informatik.uni-kl.de